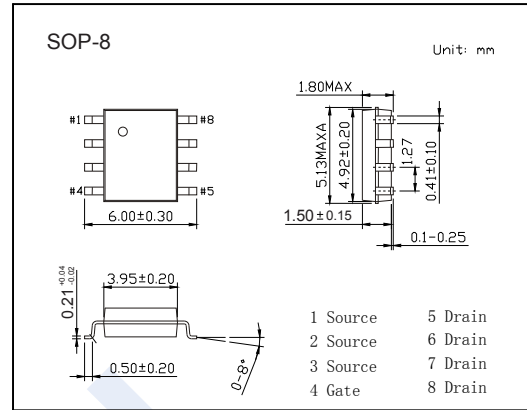
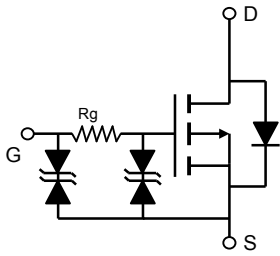


## P-Channel MOSFET

### AO4447A (KO4447A)

#### ■ Features

- $V_{DS} (V) = -30V$
- $I_D = -17 A (V_{GS} = -10V)$
- $R_{DS(ON)} < 7m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 8m\Omega (V_{GS} = -4.5V)$
- $R_{DS(ON)} < 9m\Omega (V_{GS} = -4V)$
- ESD Rating: 2000V HBM



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	$T_A = 25^\circ C$	-17
		$T_A = 70^\circ C$	-13
Pulsed Drain Current	$I_{DM}$	-160	A
Power Dissipation	$P_D$	$T_A = 25^\circ C$	3.1
		$T_A = 70^\circ C$	2
Thermal Resistance.Junction- to-Ambient	$R_{thJA}$	$t \leq 10s$	40
		Steady-State	75
Thermal Resistance.Junction- to-Lead	$R_{thJL}$	24	$^\circ C/W$
Junction Temperature	$T_J$	150	
Storage Temperature Range	$T_{stg}$	-55 to 150	$^\circ C$

## P-Channel MOSFET

### AO4447A (KO4447A)

#### ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =-250 μA, V <sub>GS</sub> =0V	-30			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V			-1	μA	
		V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			-5		
Gate-Body leakage current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±16V			±10	μA	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.8		-1.6	V	
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-17A			7	mΩ	
		V <sub>GS</sub> =-10V, I <sub>D</sub> =-17A T <sub>J</sub> =125°C			8.5		
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-15A			8		
		V <sub>GS</sub> =-4V, I <sub>D</sub> =-13A			9		
On state drain current	I <sub>D(ON)</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-5V	-160			A	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-17A		70		S	
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =-15V, f=1MHz		4580	5500	pF	
Output Capacitance	C <sub>oss</sub>			755			
Reverse Transfer Capacitance	C <sub>rss</sub>			564			
Gate resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz		160	210	Ω	
Total Gate Charge (10V)	Q <sub>g</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-15V, I <sub>D</sub> =-17A		87	105	nC	
Total Gate Charge (4.5V)				41			
Gate Source Charge			Q <sub>gs</sub>		12.8		
Gate Drain Charge			Q <sub>gd</sub>		17		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-15V, R <sub>L</sub> =0.9Ω, R <sub>GEN</sub> =3Ω		180		ns	
Turn-On Rise Time	t <sub>r</sub>			260			
Turn-Off DelayTime	t <sub>d(off)</sub>			1200			
Turn-Off Fall Time	t <sub>f</sub>			9700			
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =-17A, di/dt=300A/us		32	40	nC	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			77			
Maximum Body-Diode Continuous Current	I <sub>S</sub>				-3	A	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V			-1	V	

Note :The static characteristics in Figures 1 to 6 are obtained using <300us pulses, duty cycle 0.5% max.

#### ■ Marking

Marking	4447A
	KC****

## P-Channel MOSFET AO4447A (KO4447A)

### Typical Characteristics

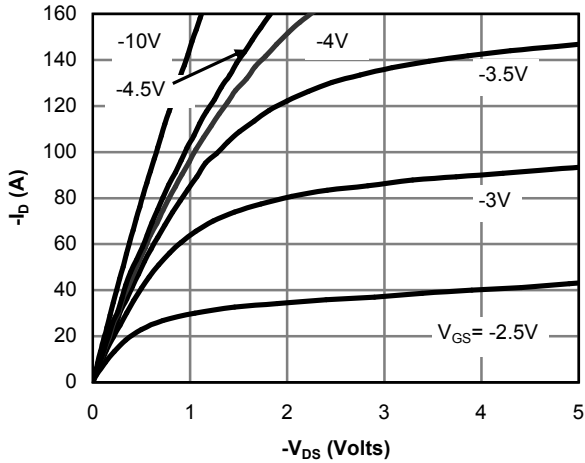


Figure 1: On-Region Characteristics(Note E)

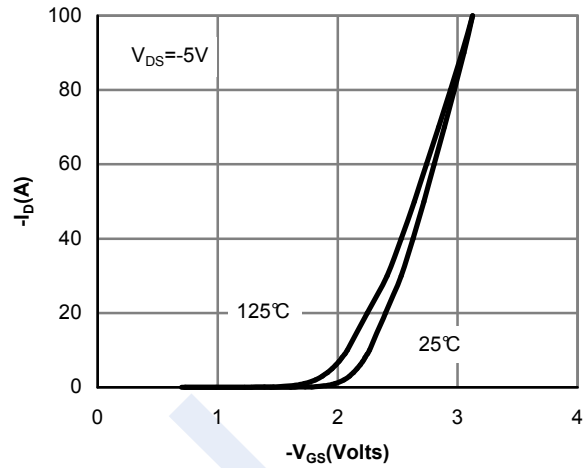


Figure 2: Transfer Characteristics(Note E)

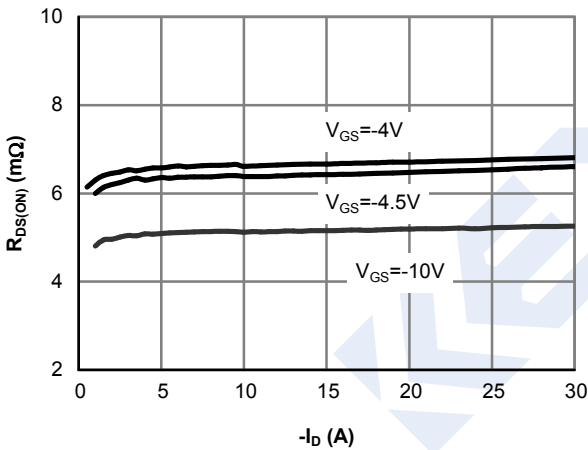


Figure 3: On-Resistance vs. Drain Current and Gate Voltage(Note E)

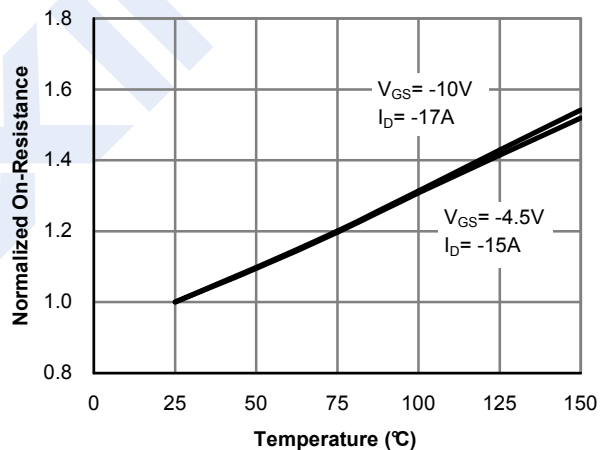


Figure 4: On-Resistance vs. Junction Temperature(Note E)

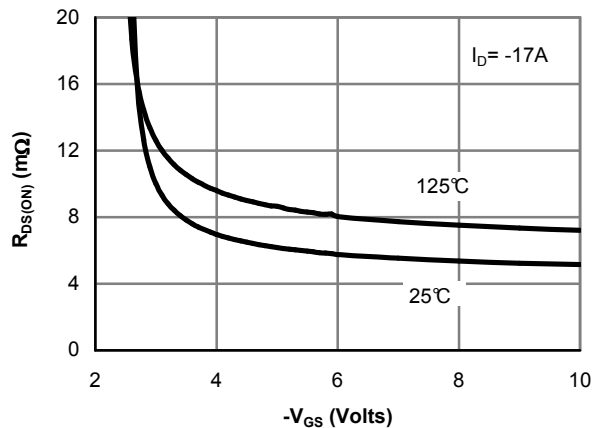


Figure 5: On-Resistance vs. Gate-Source Voltage(Note E)

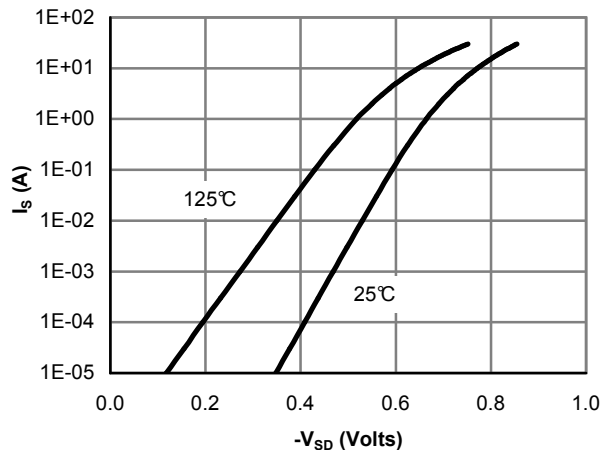


Figure 6: Body-Diode Characteristics(Note E)

## P-Channel MOSFET AO4447A (KO4447A)

■ Typical Characteristics

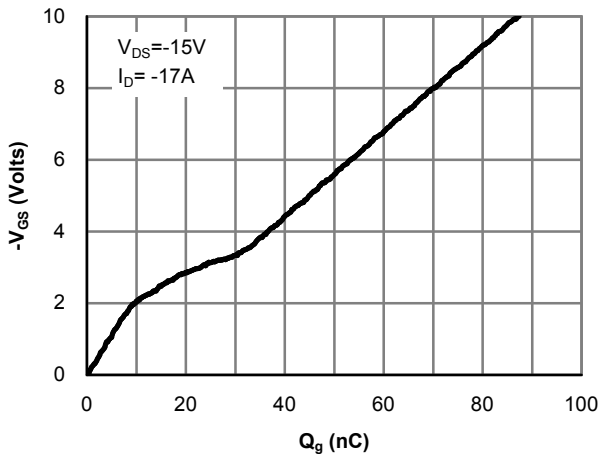


Figure 7: Gate-Charge Characteristics

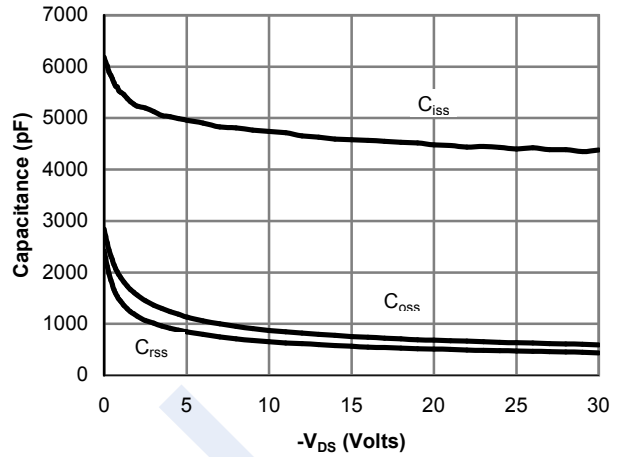


Figure 8: Capacitance Characteristics

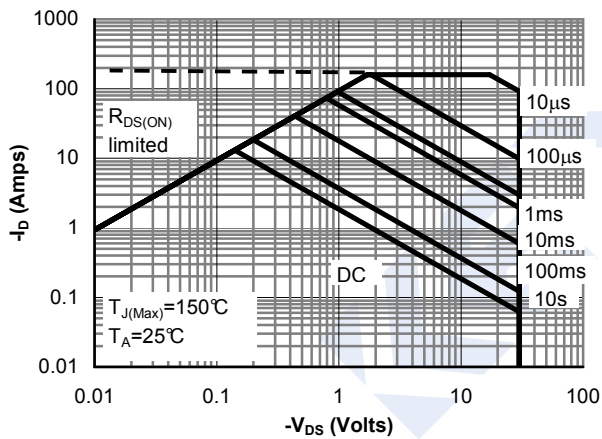


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

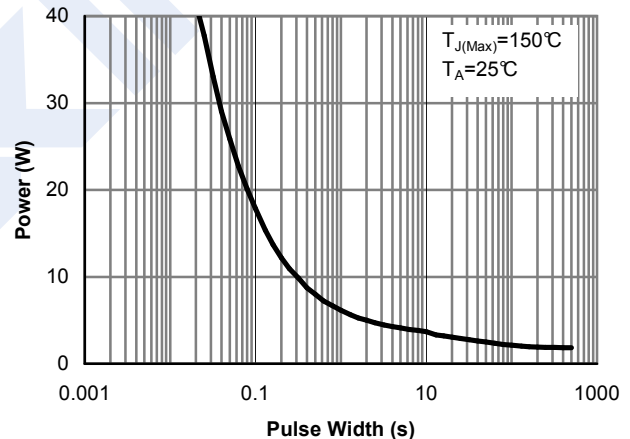


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

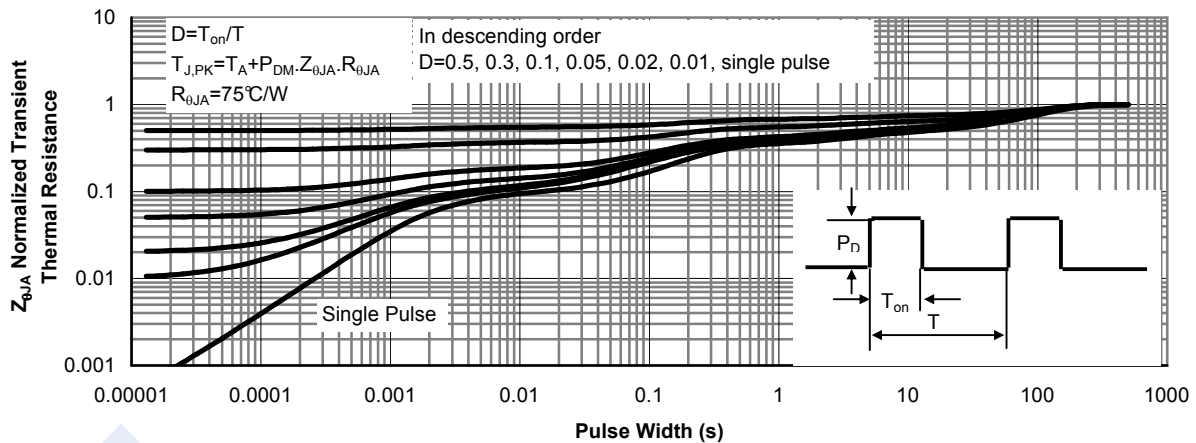


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)